



ALTERNATIVE TO PTO/SB/08a/b (08-03)

Substitute for form 1449/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Complete If Known	
				Application Number	10/626025-Conf. #1268
Filing Date		July 24, 2003			
First Named Inventor		Karl H. Torbjorn, Gardenfors			
Art Unit		2635			
Examiner Name		B. A. Zimmerman			
Sheet	1	of	2	Attorney Docket Number	34650-00179USC2

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document
		Number-Kind Code ² (if known)			
BB	A1	US-2002/0094037		07-18-2002	Darabi et al.
	A2	US-5,937,341		08-10-1999	Suominen
	A3	US-6,427,068		07-30-2002	Suominen
	A4	US-6,631,256		10-07-2003	Suominen
	A5	US-2004/0038655		02-26-2004	Suominen
	A6	US-4,944,025		07-24-1990	Gehring et al.
	A7	US-5,003,621		03-26-1991	Gailus
	A8	US-4,653,117		03-24-1987	Heck
	A9	US-5,222,144		06-22-1993	Whitehart
	A10	US-4,852,123		07-25-1989	Bickley et al.
	A11	US-4,464,770		08-07-1984	Maurer et al.
	A12	US-5,640,698		06-17-1997	Shen et al.
	A13	US-5,850,598		12-15-1998	Behrent
	A14	US-6,226,509		05-01-2001	Mole et al.

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Examiner Initials*	Cite No. ¹	Foreign Patent Document		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	T ³
		Country Code ² -Number ² -Kind Code ² (if known)				
BB	B1	-JP A 07-254865 .		10-03-1995	Toshiba Corp.	Abstract
	B2	-EP 0 742 647 A1 .		11-13-1996	Matsushita Electric	
	B3	-WO 94/10757 .		05-11-1994	Data-Radio Engineering & Consulting GmbH	
	B4	-WO 97/08842.		03-06-1997	Pacific Communications Sciences, Inc.	
	B5	-WO 98/08300 .		02-26-1998	Advanced Micro Devices,, Inc.	
	B6	-EP 0 797 292 A1		09-24-1997	Philips Electronics N.V.	
	B7	-EP 1 058 396 A1.		12-06-2000	Motorola, Inc.	
	B8	-WO96/17441 .		06-06-1996	Matsushita Electric Industrial Col. Ltd.	

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NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			T ²
BB	C1	HOOMAN DARABI et al., "A 2.4-GHz CMOS Transceiver for Bluetooth", IEEE Journal of Solid-State Circuits, Vol. 36, No. 12, December 2001			
	C2	D.M. BAKKER et al. "Bluetooth End to End", M&T Books, an imprint of Hungry Mind, Hungry Minds, Inc., ISBN: 0-7645-4887-5, 7 pages			
	C3	DAVID BLANKENBECKLER, "An Introduction to Bluetooth" ThinkBurst Media, Inc. www.wirelessdevnet.com/channels/bluetooth/features/bluetooth.html , pp. 1-7			
	C4	ING. ZDENEK MIKESKA, "Parameters of Bluetooth receiver and transmitter", January 2, 2004, Institute of Electrical Engineering and Communication, Brno University of Technology, Czech Republic, pp. 1-9			

Examiner Signature	BRIAN ZIMMERMAN PRIMARY EXAMINER	Date Considered	5/4/05
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66	C5	BROADCOM "BCM2002X 2.4GHz Bluetooth Frac-N-Radio" Product Brief	
	C6	PIETRO ANDREANI et al. "A CMOS gm-C IF Filter for Bluetooth", in Proceedings of CICC 2000, paper 18-6, May 2000, 4 pages	
	C7	JAN CROLS et al., "A Single-Chip 900 MHz CMOS Receiver Front-End with a High Performance Low-IF Topology", IEEE Journal of Solid-State Circuits, Vol. 30, No. 12, December 1995, Katholieke Universiteit Leuven, Heverlee, Belgium	
	C8	ASAD A. ABIDI, "Direct-Conversion Radio Transceivers for Digital Communications," IEEE Journal of Solid-State Circuits, Vol. 30, No. 12, December 1995, Katholieke Universiteit Leuven, Heverlee, Belgium, pp. 1390-1411	
	C9	M. STEYAERT, "A 1-GHz Single-Chip Quadrature Modulator", IEEE Journal of Solid-State Circuits, Vol. 27, No. 8, August 1992, pps. 1194-1197	
	C10	JACK P.F. GLAS, "Digital I/Q Imbalance Compensation in a Low-IF Receiver" IEEE Globecom, pp 1461-1466, Nov. 1998	
	C11	VITO BOCCUZZI et al., "Testing the Double Low-IF Receiver Architecture" Proceedings of the CICC, IEEE, 1997, pp. 521-524	
	C12	VALKAMA et al., "BSS Based I/Q Imbalance Compensation in Communication Receivers in the Presence of Symbol Timing Errors", Proc. Second Int. Workshop Independent Component Analysis Blind Signal Separation, Helsinki, Finland, June 2000, pp. 393-398.	
	C13	J. SEVENHANS et al., "An Analog Radio Front-end Chip Set for a 1.9 GHz Mobile Radio Telephone Application" 1994 IEEE International Solid-State Circuits Conference, February 16, 1994	
	C14	J.O. VOORMAN "Continuous-time analog integrated filters" Integrated Continuous-Time Filters, New York IEEE Press, 1993	
	C15	JAN CROLS et al., "An Analog Integrated Polyphase Filter for a High Performance Low-IF Receiver", 1995 Symposium on VLSI Circuits Digest of Technical Papers, Katholieke Universiteit Leuven, Heverlee, Belgium	
	C16	M.J. GINGELL, "Single Sideband Modulation using Sequence Asymmetric Polyphase Networks" Electrical Communication - Volume 48, Number 1 and 2 - 1973	
	C17	STEPHEN A. JANTZI, "A Fourth-Order Bandpass Sigma-Delta Modulator", IEEE Journal of Solid-State Circuits, Vol. 28, No. 3, March 1993	
	C18	JAN CROLS et al., "A 1.5 GHz Highly Linear CMOS Downconversion Mixer" IEEE Journal of Solid-State Circuits, Vol 30, No. 7, July 1993	
	C19	TRUDY D. STETZLER ET AL., "A 2.7-4.5 V Single Chip GSM Transceiver RF Integrated Circuit, IEEE Journal of Solid-State Circuits, Vol. 30, No. 12, December 1995	
66	C20	FLOYD M. GARDNER, "Phaselock Techniques, "Phaselock Receivers and Transponders" ISBN: 0-471-04294-3, April 1979	

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¹Applicant's unique citation designation number (optional). ²Applicant is to place a check mark here if English language Translation is attached.

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